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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Dell

Serial No.: 09/800,145

Filing Date: March 6, 2001

Docket No.: 4015-815

Title: Recursive Cryptoaccelerator and Recursive  
VHDL Design of Logic Circuits

Examiner: Paula W. Klimach

Group Art Unit: 2135

Confirmation No. 4516

Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 22, 2005.

  
Kathleen Koppen

**RESPONSE TO NON-FINAL OFFICE ACTION**

This paper is being filed in response to the Office Action mailed November 22, 2004 having a reply due date of February 22, 2005. Reconsideration and reexamination are respectfully requested in light of the amendments and remarks below. While no fees should be required for entry of this response, if any fees or charges are required, the Commissioner is hereby authorized to charge them to Deposit Account 18-1167.